



CLK & CLKb – 40MHz input clock (LVDS level)
 OUT & OUTb – output (LVDS level)

TERMINATION: OUTb — 120 Ohm — OUT
 CLKb — 120 Ohm — CLK

Vcc – analogue power supply (+3.5V)
 VDD – digital power supply (+4V)
 GND – ground (0V)

LEAKAGE: VDD → GND